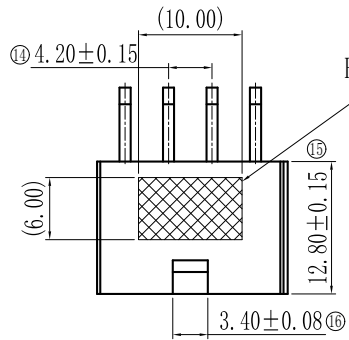


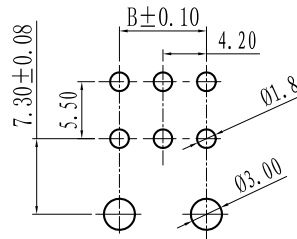
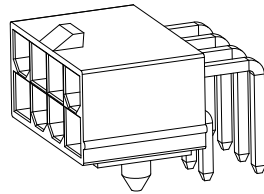
Terminal number	A	B
02	5.40	-
04	9.60	-
06	13.80	8.40
08	18.00	12.60
10	22.20	16.80
12	26.40	21.00
14	30.60	25.20
16	34.80	29.40
18	39.00	33.60
20	43.20	37.80
22	47.40	42.00
24	51.60	46.20

Performance characteristics:

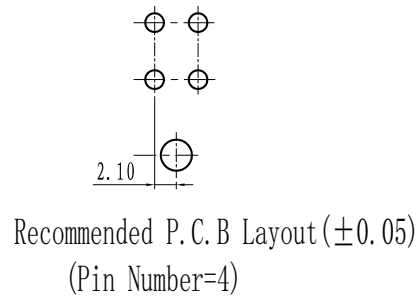
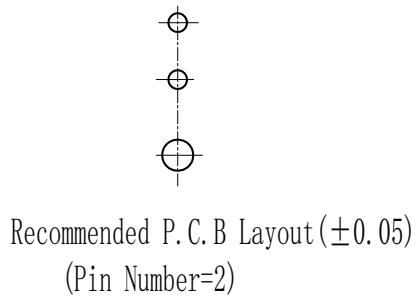
- Main body part material  
Housing: see spec list  
Contact terminal: brass, 1.27~2μm Ni under plated, outer plating see spec list
- Electrical requirements:  
Current rating: 9A/pin.  
Voltage rating: 600 V  
Contact resistance: ≤10 mΩ  
Insulation resistance: ≥1000 MΩ  
Withstanding voltage: 1500 Vr.m.s, 1mA, 60s
- Mechanical requirements  
Retention force: ≥10 N/pin
- Environment specification  
Working temperature: -40°C~105°C  
Humidity: ≤93% at 40°C  
Resistance to soldering heat: 260°C, 30s
- Packing & storing  
Method of packing: Tape&Reel, outer box  
Storing condition: temperature of -10°C~+40°C below 80% relative humidity
- Style of installation: solder pcb installation(PIP)
- To prevent the pollution of the environment product info RoHS agreement: In line with the European Union RoHS.



Recommended Adsorbable Area



Recommended P.C.B Layout (±0.05)  
(Pin Number ≥6)



1348 - XX X - X X - X

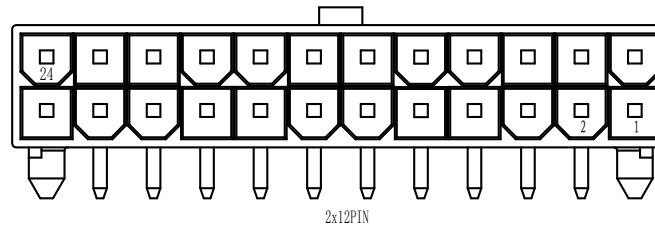
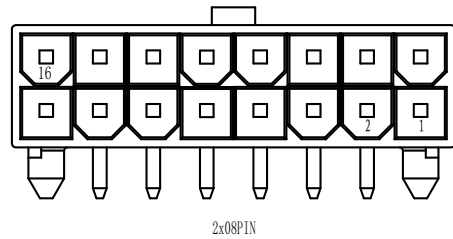
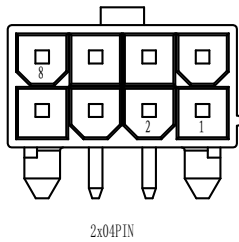
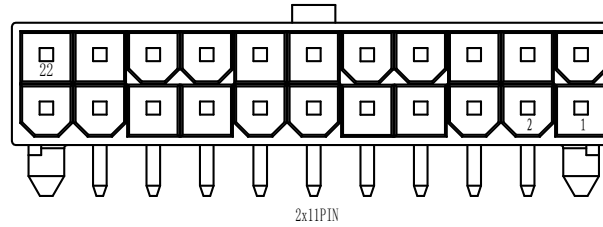
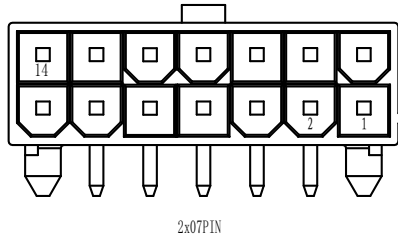
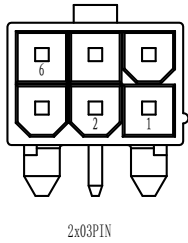
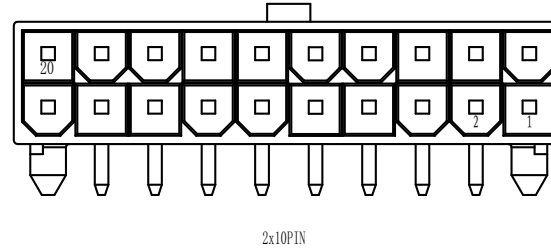
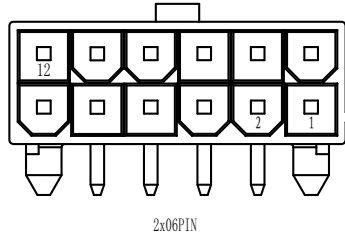
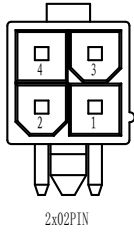
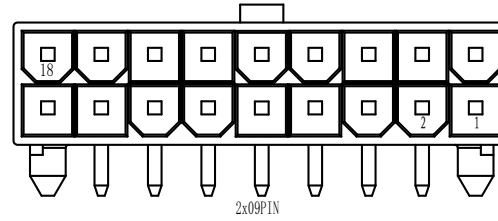
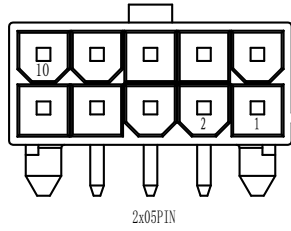
Series code	Terminal number	Spec code	Spec code	Plating code	Insulator material & color
	02~24	Code Definition	Code Definition	Code Definition	Code Definition
		4 Dual row with pegs	H R/A With pegs L=3.20	Contact area Tail	3 LCP BLACK UL94V-0
			G R/A With pegs L=2.60	B Matte Sn 3.81μm Min	N LCP NATURAL UL94V-0
			X Customized	X Customized	X Customized

Unless otherwise specified tolerance:

X.	±0.50	X.°	±3°
.X	±0.38	.X°	±2°
.XX	±0.25		

Mark	NO.	File NO.	Signature	Date	4.20mm Pitch Wafer Dual Row R/A With Pegs	SLO. 1348. 031			
DRAW			James Zhang	2019.06.26		Projection	Rev.	Weight	Scale
CHK			Tim	2019.07.04	AO				
MAN			laijuanli	2019.07.05	All Dimensions in mm Sheet 1 of 2				
STD			Alan	2019.07.05	1348-XXX-XX-X				
APP					SHINNING SHENZHEN SHINNING ELECTRONIC CO.LTD.				

# Diagram of All Circuit



Unless otherwise specified tolerance:

X.	±0.50	X.°	±3°
.X	±0.38	.X°	±2°
.XX	±0.25		

Mark	NO.	File NO.	Signature	Date
DRAW			James Zhang	2019.06.26
CHK			<b>Tim</b>	<b>2019.07.04</b>
MAN			<b>laijuanli</b>	<b>2019.07.05</b>
STD			<b>Alan</b>	<b>2019.07.05</b>
APP				

4.20mm Pitch Wafer  
Dual Row R/A With Pegs

1348-XXX-XX-X

SLO. 1348.031

Projection	Rev.	Weight	Scale
	A0		
All Dimensions in mm		Sheet 2 of 2	
SHINNING SHENZHEN SHINNING ELECTRONIC CO.,LTD.			